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PTO/SB/08A (10-96)

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				Application Number	
				Filing Date	
				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	
				BP 1989	

[illegible][illegible]

Examiner Signature		Date Considered	
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				Application Number	
				Filing Date	12/12/01
				First Named Inventor	Chien, et al
				Group Art Unit	
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	BP 1989

Examiner Initials *	Cite No. 1	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ¹
BT	1	"A 27-mW CMOS Fractional-N Synthesizer Using Digital Compensation for 2.5Mb/s GFSK Modulation", Michael H. Perrott, Student Member, IEEE, Theodore L. Tewksbury III, Member, IEEE, and Charles G. Sodini, Fellow, IEEE - IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 32, NO. 12, December 1997,	<input type="checkbox"/>
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